

AMENDMENTS TO THE CLAIMS

This Listing of Claims will replace all prior versions, listing, of claims in the specification.

LISTING OF CLAIMS:

Claim 1 (original) A semiconductor test system having a tester and a prober comprising a control board disposed on a test interface of said tester, said control board comprising:

a discrimination circuit comprising a comparison circuit, a determination circuit and a control circuit; and

a protection circuit;

whereby said comparison circuit will compare signals at a test side and at an interface side and send a comparison result to said determination circuit before said tester receives a tester start signal sent out by said prober, said determination circuit will determine whether the connection is correct according to the comparison result and then send a determination result to said control circuit, said control circuit will activate said protection circuit to interrupt said tester start signal according to the determination result if the connection is incorrect, hence disabling said test system.

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Claim 2 (original) The semiconductor test system as claimed in Claim 1, wherein said control circuit can further drive a lamp to be on and get through said tester start signal to let said test system start testing when said determination circuit determines the connection is correct.

Claim 3 (original) The semiconductor test system as claimed in Claim 1, wherein said control circuit can further send out an alarming signal to an alarm to inform operation staffs for check and correction when said determination circuit determines the connection is incorrect.

Claims 4-13 (canceled).